

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In application of:

Ajit P. Paranjpe et al.

Serial No.:

09/553,993

Filing Date:

April 21, 2000

Examiner:

Berezny, Neal

Group Art No.:

2823

Title:

***Method for Fabricating a Semiconductor Chip
Interconnect***

Honorable Assistant Commissioner
of Patents
Washington D.C. 20231

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Dear Sir:

NOTIFICATION OF CHANGE IN SMALL ENTITY STATUS

The assignee of the above-identified U.S. Patent Application wishes to notify the U.S. Patent and Trademark Office that status as a Small Entity is no longer appropriate pursuant to 37 C.F.R. §1.9.

Respectfully submitted,
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